# Log book

The purpose of this document is to keep track of the project on a weekly basis. Each week includes planned objectives, details of work done, what was actually achieved and any comments regarding the work done during that week.

# Semester 1:

Semester one includes mainly of research material required for the project.

## Week 11: 20 November 2017

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| **Objectives:** | **Completion:** | **Comments:** |
| Research Pulse Width Modulation |  |  |
| Gate driver circuits |  |
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Objectives for this week are:

* To Research Pulse Width Modulation
* To Gate driver circuits

***Pulse width modulation (PWM):***

The PWM required for the class D- amplifier is considered. Triangle wave oscillator & voltage comparator required for modulation of input signal.

Ways of producing a triangular wave oscillation:

1. 555 Timer.
2. Integrator in series with a Schmitt trigger.
3. Using a LM741 op-amp

Suitable voltage comparators:

1. LM741 op-amp

***Gate driver:***

Finished tasks

Comments